## **REMARKS**

Claims 1-4, 6, 8, 11, 13, 16 have been amended.

Claims 5, 7, 9, 10, 12 remain in this application.

Claims 14-15 have been cancelled.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "Version with markings to show changes made."

Applicant respectfully requests that a timely Notice of Allowance be issued in this case. Respectfully submitted,

Han-Yi Lee

Agent for Applicant

Reg. No. 53,192

(510) 579-6211

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to - Commissioner

for Patents, Alexandria, VA 22313

I hereby certify that this convey ...

war and one States Postal Service as first class mail in an envelope addressed to - Commissioner

for Patents, Alexandria, VA 22313

## "Version with markings to show changes made."

1. (Currently Amended) A state-displaying device for displaying state data generated by a data-processing device, said state-displaying device comprises:

an universal asynchronous receiver/transmitter interface for receiving state data for outputting said state data in a serial mode; and

a displaying device including:

- (a) a microprocessor coupled to said universal asynchronous receiver/transmitter interface for outputting a displaying signal in corresponding to said state data output by said universal asynchronous receiver/transmitter interface; and
- (b) a multi-segment display module coupled to said microprocessor for displaying a symbol in corresponding to said displaying signal;

wherein said state data is generated by a BIOS program of said data-processing device and/or said state data is generated by a detecting application program of said data-processing device.

- 2. (Currently Amended) The state-displaying device as in claim 1, wherein said multi-segment display module [at least] is composed of <u>at least one</u> seven- segment display.
- 3. (Currently Amended) The state-displaying device as in claim 1, wherein said data-processing device is [selected between] a server [and] or a personal computer.
- 4. (Currently Amended) The state-displaying device as in claim 1, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

- 5. (Original) The state-displaying device as in claim 1, wherein said universal asynchronous receiver/transmitter interface includes a data transmitting line Tx, a data receiving line Rx, a power line and a grounding line (Gnd).
- 6. (Currently Amended) The state-displaying device as in claim 1, wherein said state data output by said universal asynchronous receiver/ transmitter interface [is of a] complies the specification of RS-232.
- 7. (Original) The state-displaying device as in claim 1, wherein said state-displaying device is connected externally to a serial port.
- 8. (Currently Amended) The state-displaying device as in claim 1, wherein said state data includes an on/off bit, at least [a] one command mode bit and a plurality of displaying bits, said command mode bit is used to define a mode of displaying of said displaying bits, said microprocessor decides a mode of displaying of said multi- segment display module according to said mode of displaying of said displaying bits.
- 9. (Original) The state-displaying device as in claim 8, wherein said command mode bit is used to decide between a searching mode and a following-the-sequence mode.
- 10. (Original) The state-displaying device as in claim 9, wherein when said microprocessor decides that a mode of displaying of said multi-segment display module is said searching mode, it makes said multi-segment display module to display said symbol

according to said displaying signal which is generated by searching in a table according to values of said displaying bits.

11. (Currently Amended) The state-disp

The state-displaying device as in claim 10, wherein

said symbol is [selected among] a numeral, an English letter [and] or a specific character.

12. (Original) The state-displaying device as in claim 9, wherein

when said microprocessor decides that a mode of displaying is said following-the-sequence

mode, a selecting bit of said displaying bits is used to designate a seven-segment display to

be enabled, and said enabled seven-segment display is rendered to display said symbol

according to the state of a plurality of segment-selecting bits of said displaying bits.

13. (Currently Amended) The state-displaying device as in claim 12, wherein

said symbol is [selected among] a numeral, an English letter [and] or a specific character.

14. (Cancelled)

15. (Cancelled)

16. (Currently Amended)  $\underline{14}$  [16]. The state-displaying device as in claim  $\underline{1}$ 

[15], wherein said detecting application program is executed in an operating system of said

data-processing device.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to - Commissioner

for Patents, Alexandria, VA 22313

on Avg 05 ,20 -

Attorney for Applicant

8